

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Thomas, et al.

Docket No.:

2003 P 54322 US

Serial No.:

10/790,907

Art Unit:

2811

Filed:

March 2, 2004

Examiner:

**TBD** 

For:

Integrated Circuit with Re-Route Layer and Stacked Die Assembly

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PTO/SB/08A & 08B with 17 References Cited (2 pages)
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Respectfully submitted,

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## INFORMATION DISCLOSURE STATEMENT

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits. In the event a fee is required, please charge any additional amount, or credit any overpayment to Deposit Acct. No.50-1065 of the below mentioned firm.

Pursuant to 37 CFR 1.98(a)(2)(i), as amended, copies of U.S. Patents cited are not being submitted. However, Applicant has included copies of any non-patent literature.

Respectfully submitted,

June 15, 2004

Date

Ira S. Matsil

Attorney for Applicant

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Sheet	1	of	2	Attorney Docket Number	2003 P 54322 US	

			U.S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number  Number - Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-6,291,881 B1	09-18-2001	Yang	
	2	US-6,495,442 B1	12-17-2002	Lin, et al.	
	3	US-6,514,794 B2	02-04-2003	Haba, et al.	
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	5	US-6,560,117 B2	05-06-2003	Moon	
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			N PATENT DOCU	IMENTS		
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document		T <sub>6</sub>
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 'Applicant's unique citation designation number (optional). 'See Kinds Codes of USPTO Patent Documents at <a href="www.uspto.gov">www.uspto.gov</a> or MPEP 901.04. 'Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 'Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 'Applicant is to place a check mark here if English language Translation is attached.

Translation is attached.

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der the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Substitute for form 1449B/PTO Complete if Known Application Number 10/790,907 INFORMATION DISCLOSURE Filing Date March 2, 2004 First Named Inventor Thomas, et al. STATEMENT BY APPLICANT Art Unit 2811 Examiner Name **TBD** (Use as many sheets as necessary) Sheet 2 Attorney Docket Number 2003 P 54322 US

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>		
***	17	KIM, G., "Pad-On-Circuit (PoC) Process and Its Application," 10th Annual International KGD Packaging & Test Workshop, Napa, CA (September 8-10, 2003) 27 pages.			
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